

U.S. PTO
09/539206
03/30/00

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| Subclass | ISSUE CLASSIFICATION |
| Class | |
| | |

PATENT NUMBER

U.S. UTILITY Patent Application

| | |
|-------------------|----------------|
| O.I.P.E. | PATENT DATE |
| SCANNED <i>SW</i> | O.A. <i>CK</i> |

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|------------------------------|-----------------|--------------|-----------------------|-------------------------|--------------------------|
| APPLICATION NO. 09/539206 | CONT/PRIOR F | CLASS 360 | SUBCLASS <i>61</i> | ART UNIT <i>2750</i> | EXAMINER <i>Colon</i> |
|------------------------------|-----------------|--------------|-----------------------|-------------------------|--------------------------|

APPLICANTS
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Hiroyuki Nakahira
Akira Yamamoto

68 2651

TITLE
Write compensation circuit and signal interpolation circuit of
recording device

PTO-2040
12/99

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| ISSUING CLASSIFICATION | | | | | |
|---|----------|--------------------|-----------------------------------|--|--|
| ORIGINAL | | CROSS REFERENCE(S) | | | |
| CLASS | SUBCLASS | CLASS | SUBCLASS (ONE SUBCLASS PER BLOCK) | | |
| INTERNATIONAL CLASSIFICATION | | | | | |
| <input type="checkbox"/> Continued on Issue Slip Inside File Jacket | | | | | |

| TERMINAL DISCLAIMER | DRAWINGS | | | CLAIMS ALLOWED | |
|---|-------------------------------------|-------------|------------|----------------------------|----------------------|
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims | Print Claim for O.G. |
| <input type="checkbox"/> The term of this patent subsequent to _____ (Date) has been disclaimed. | (Assistant Examiner) (Date) | | | NOTICE OF ALLOWANCE MAILED | |
| <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ | (Primary Examiner) (Date) | | | ISSUE FEE | |
| <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed. | (Legal Instruments Examiner) (Date) | | | ISSUE BATCH NUMBER | |

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